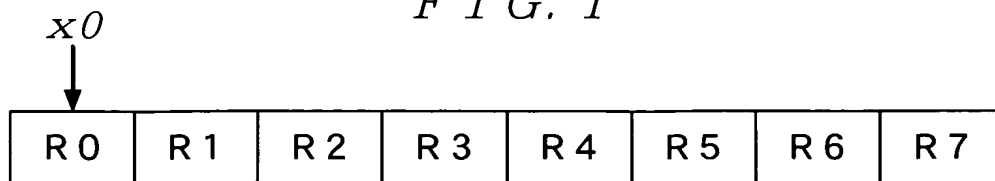


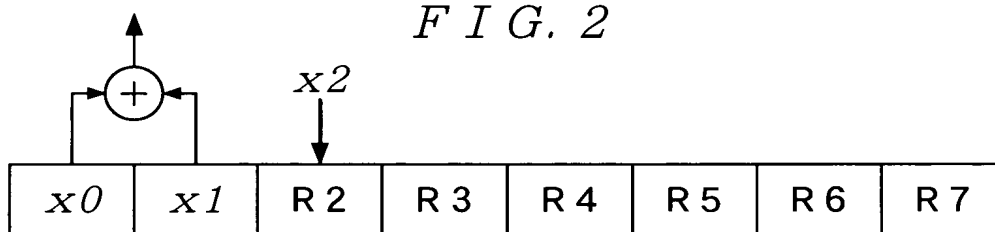
1 / 7

F I G. 1



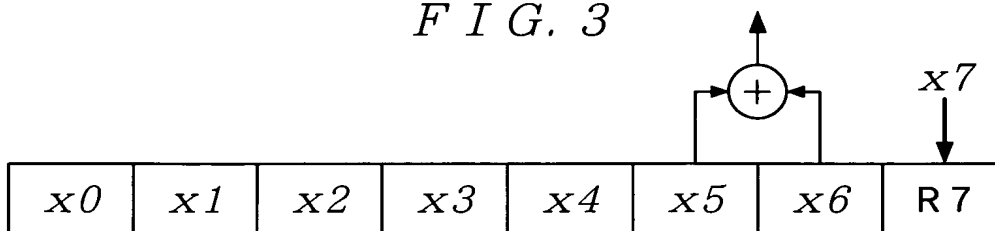
Cycle = "0"

F I G. 2



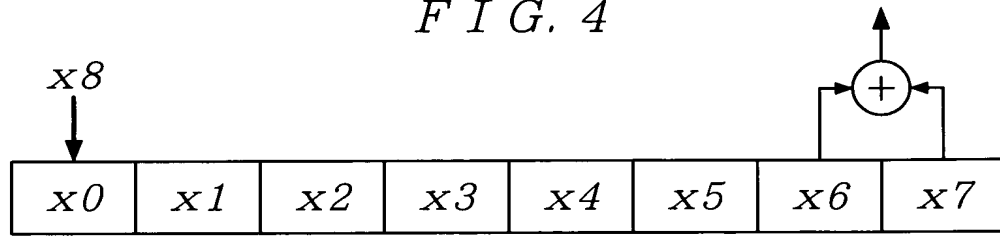
Cycle = "2"

F I G. 3



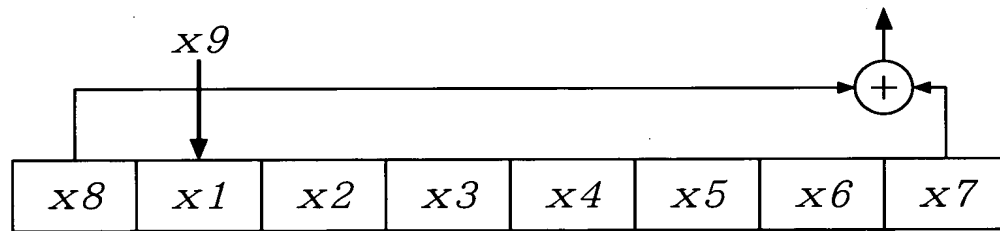
Cycle = "7"

FIG. 4



Cycle = "8"

FIG. 5



Cycle = "9"

FIG. 6

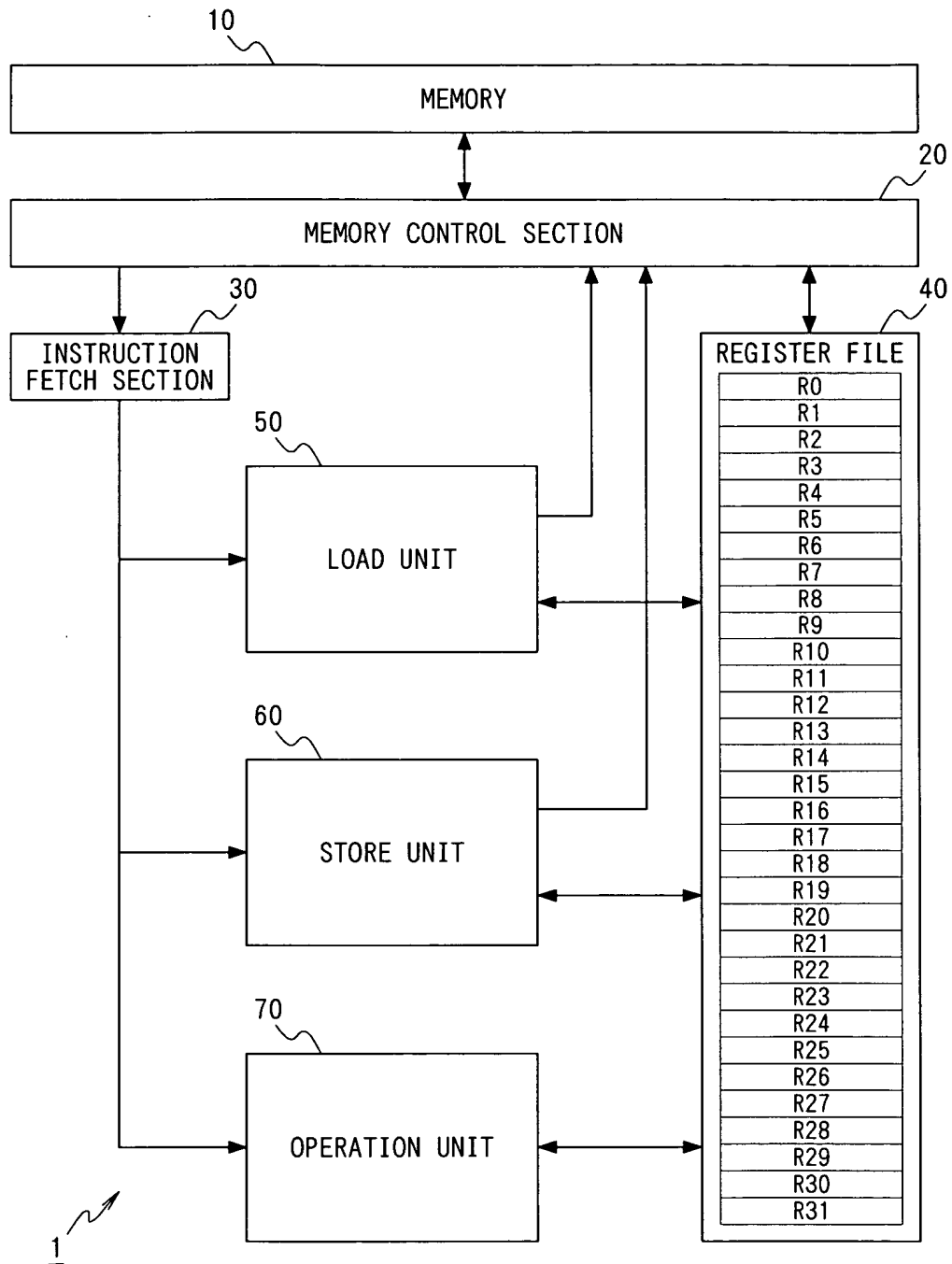


FIG. 7A

FORMAT OF LOAD INSTRUCTION

OPERATION CODE	NUMBER OF VECTOR ELEMENTS	LOAD REGISTER SPECIFICATION CODE	BASE ADDRESS REGISTER SPECIFICATION CODE	ADDRESS MODIFICATION REGISTER SPECIFICATION CODE
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FIG. 7B

FORMAT OF STORE INSTRUCTION

OPERATION CODE	NUMBER OF VECTOR ELEMENTS	STORE REGISTER SPECIFICATION CODE	BASE ADDRESS REGISTER SPECIFICATION CODE	ADDRESS MODIFICATION REGISTER SPECIFICATION CODE
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FIG. 7C

FORMAT OF OPERATION INSTRUCTION

OPERATION CODE	NUMBER OF VECTOR ELEMENTS	DESTINATION REGISTER SPECIFICATION CODE	FIRST SOURCE REGISTER SPECIFICATION CODE	SECOND SOURCE REGISTER SPECIFICATION CODE
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FIG. 8

REGISTER	CODE	REGISTER	CODE
R0	00000	R16	10000
R1	00001	R17	10001
R2	00010	R18	10010
R3	00011	R19	10011
R4	00100	R20	10100
R5	00101	R21	10101
R6	00110	R22	10110
R7	00111	R23	10111
R8	01000	R24	11000
R9	01001	R25	11001
R10	01010	R26	11010
R11	01011	R27	11011
R12	01100	R28	11100
R13	01101	R29	11101
R14	01110	R30	11110
R15	01111	R31	11111

FIG. 9

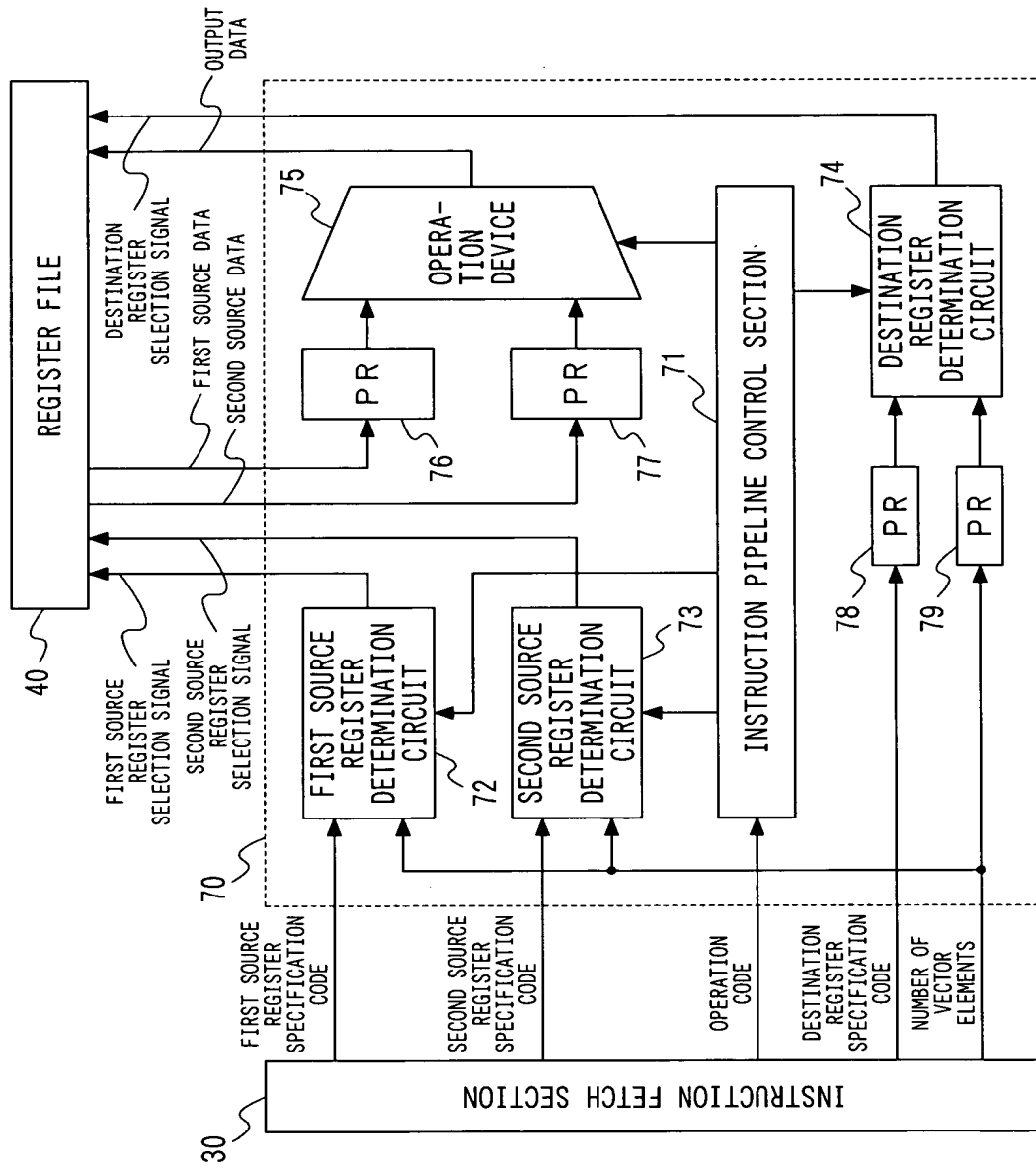


FIG. 10

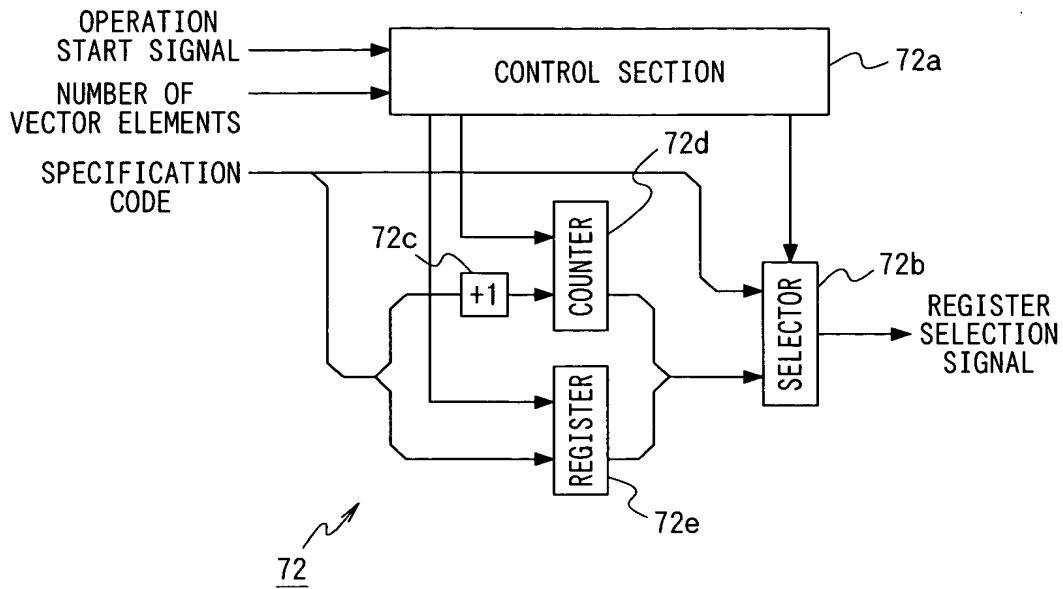


FIG. 11

NUMBER OF VECTOR ELEMENTS	OPERATION TO BE PERFORMED
0 0 0	OPERATION USING 1 ELEMENT (SCALAR OPERATION)
0 0 1	OPERATION USING 2 ELEMENTS
0 1 0	OPERATION USING 3 ELEMENTS
0 1 1	OPERATION USING 4 ELEMENTS
1 0 0	OPERATION USING 5 ELEMENTS
1 0 1	OPERATION USING 6 ELEMENTS
1 1 0	OPERATION USING 7 ELEMENTS
1 1 1	OPERATION USING 8 ELEMENTS